

"CO DIALTONE LEARNING SEQUENCE" FLOW MODEL

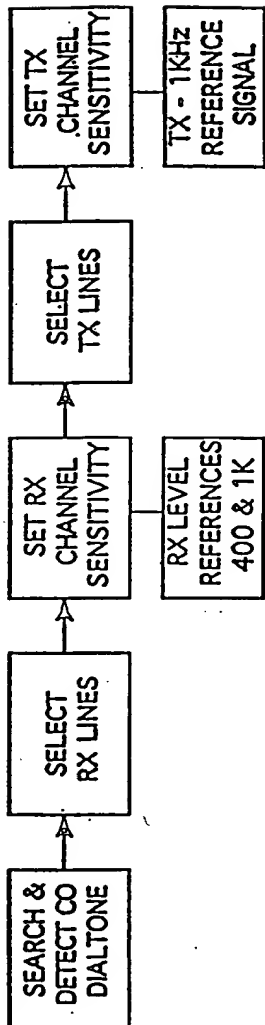


FIG. 1

"AUTOMATED 800 LEARNING SEQUENCE" FLOW MODEL

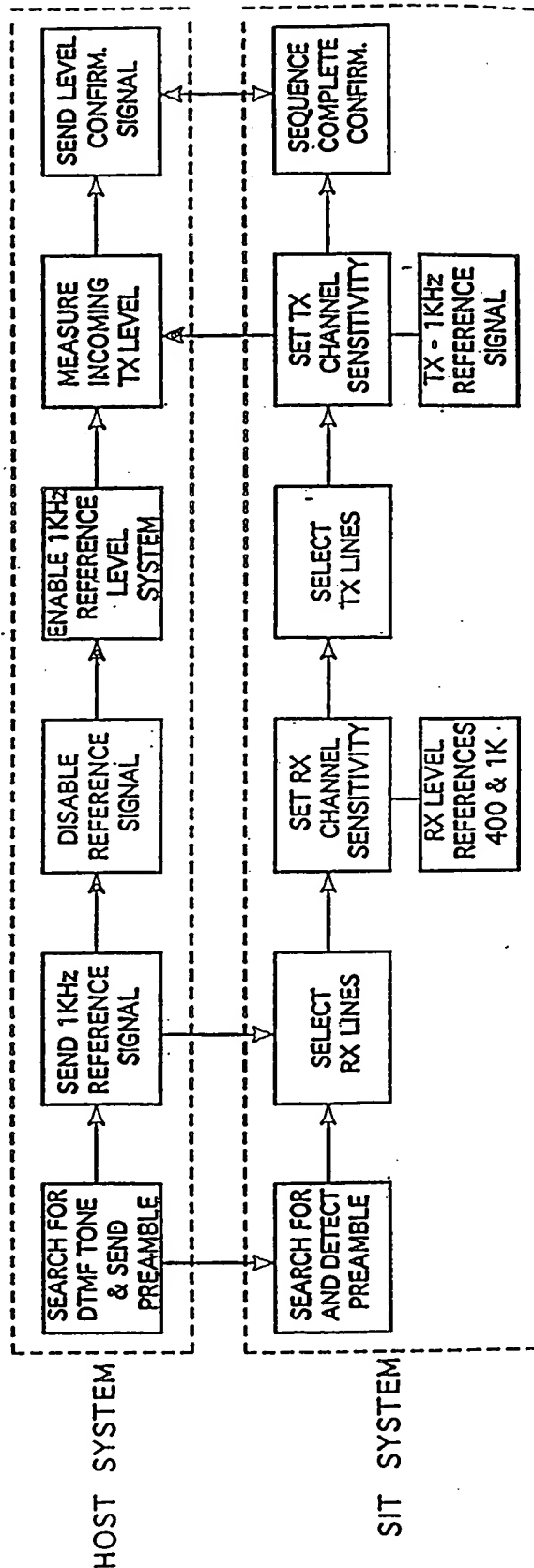


FIG. 2

0405349-0405349

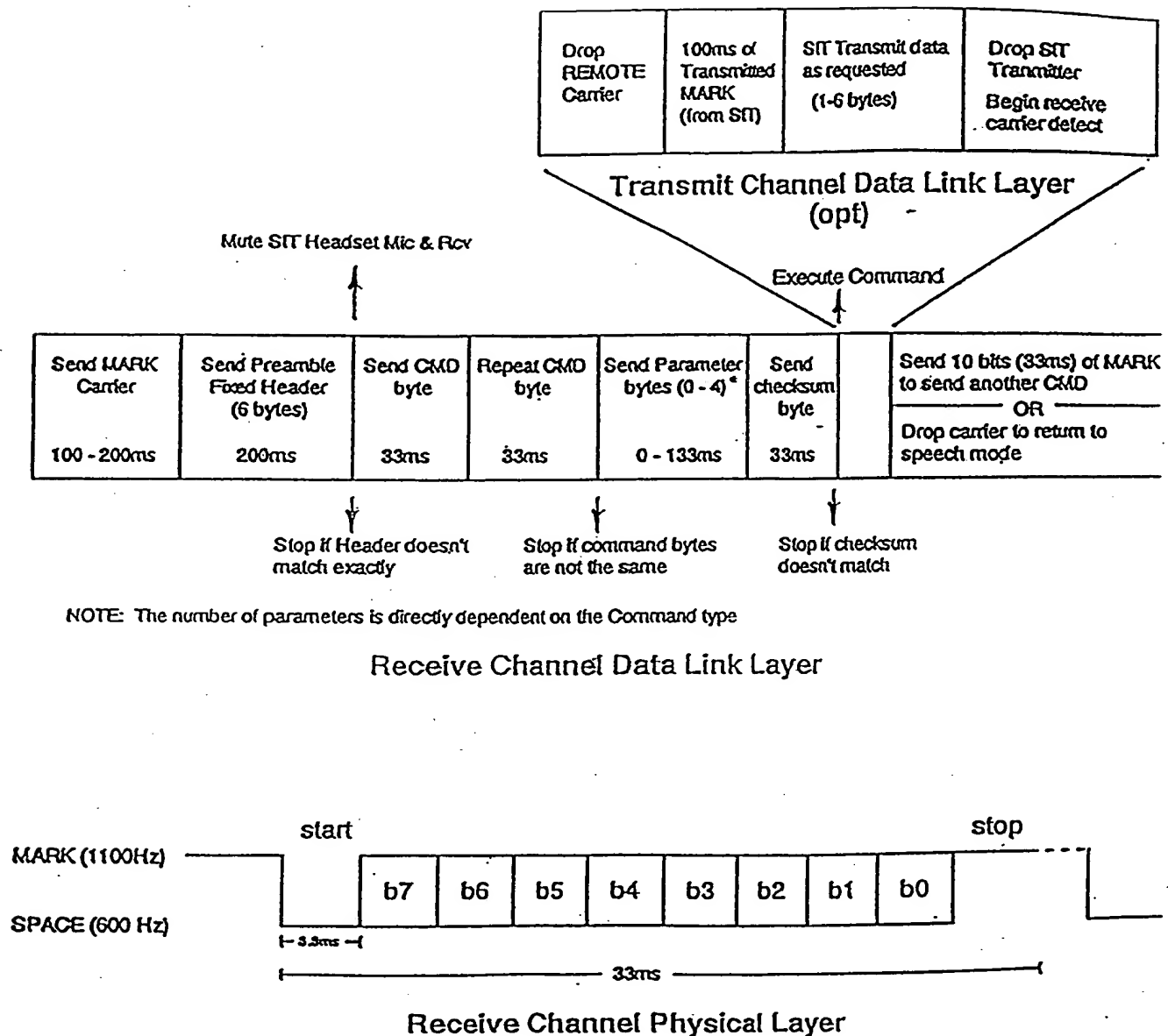
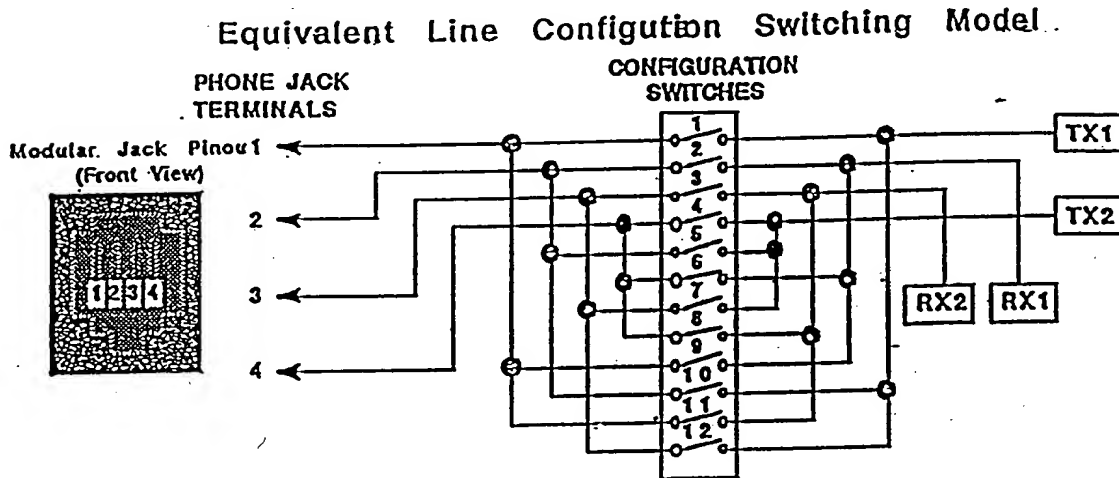


FIG. 3

Smart Interface Technology (SIT) Project

Preliminary Line Configuration Switching Algorithms and Model



<u>Dialtone Detection</u>	<u>Rx Pinout</u>	<u>Tx Pinout Sequence'</u>	<u>Algorithm Ref. No.</u>	<u>Model Sw. "ON"</u>
Pins 2 & 3	Pins 2 & 3	Pins 1 & 4 (Primary)	1	1,2,3,4
Pins 2 & 3	Pins 2 & 3	Pins 1 & 2 (2nd Try)	2	1,2,3,5
Pins 2 & 3	Pins 2 & 3	Pins 1 & 3 (3rd Try)	3	1,2,3,7
Pins 2 & 3	Pins 2 & 3	Pins 4 & 2 (4th Try)	4	2,3,4,10
Pins 2 & 3	Pins 2 & 3	Pins 4 & 3 (5th Try)	5	2,3,4,12
Pins 2 & 4	Pins 2 & 4	Pins 1 & 3 (Primary)	6	1,2,7,8
Pins 2 & 4	Pins 2 & 4	Pins 1 & 2 (2nd Try)	7	1,2,5,8
Pins 2 & 4	Pins 2 & 4	Pins 1 & 4 (3rd Try)	8	1,2,4,8
Pins 2 & 4	Pins 2 & 4	Pins 3 & 2 (4th Try)	9	2,5,8,12
Pins 2 & 4	Pins 2 & 4	Pins 3 & 4 (5th Try)	10	2,4,8,12
Pins 1 & 2	Pins 1 & 2	Pins 3 & 4 (Primary)	11	2,4,11,12
Pins 1 & 2	Pins 1 & 2	Pins 3 & 1 (2nd Try)	12	1,2,11,12
Pins 1 & 2	Pins 1 & 2	Pins 3 & 2 (3rd Try)	13	2,5,11,12
Pins 1 & 2	Pins 1 & 2	Pins 4 & 1 (4th Try)	14	1,2,4,11
Pins 1 & 2	Pins 1 & 2	Pins 4 & 2 (5th Try)	15	2,4,10,11
Pins 1 & 3	Pins 1 & 3	Pins 2 & 4 (Primary)	16	3,4,9,11
Pins 1 & 3	Pins 1 & 3	Pins 2 & 1 (2nd Try)	17	1,3,5,9
Pins 1 & 3	Pins 1 & 3	Pins 2 & 3 (3rd Try)	18	3,5,9,12
Pins 1 & 3	Pins 1 & 3	Pins 4 & 1 (4th Try)	19	1,3,4,9
Pins 1 & 3	Pins 1 & 3	Pins 4 & 3 (5th Try)	20	3,4,9,12

FIG. 4

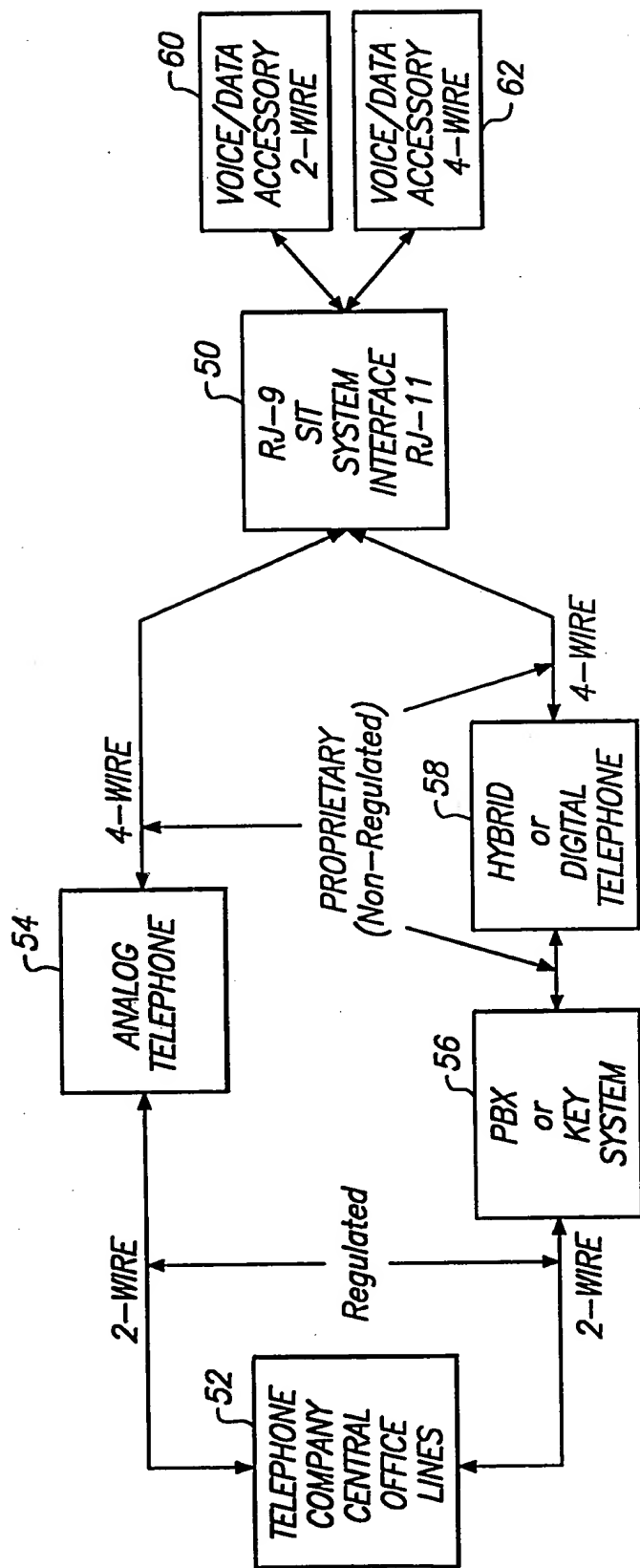


FIG. 6

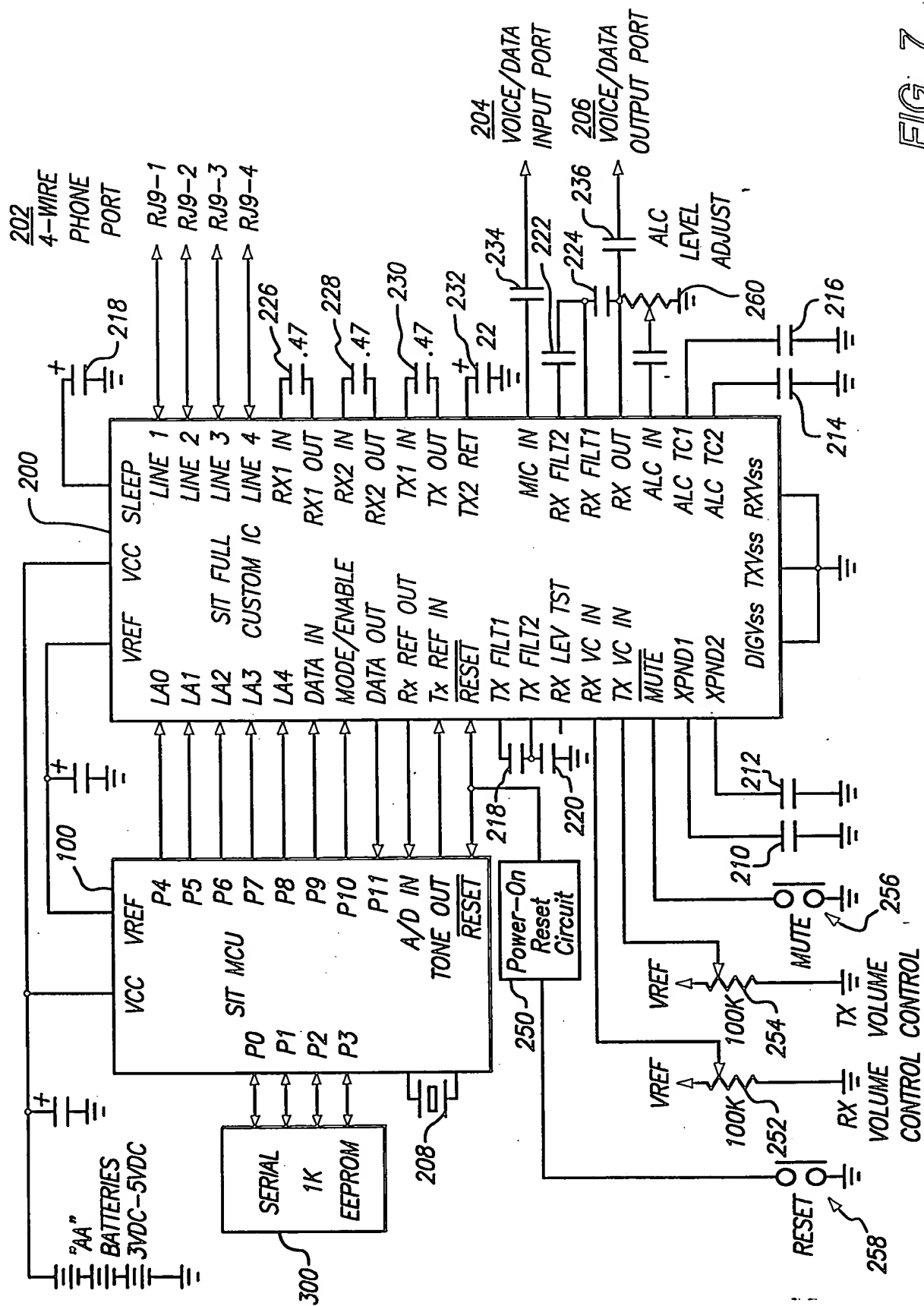


Fig. 7



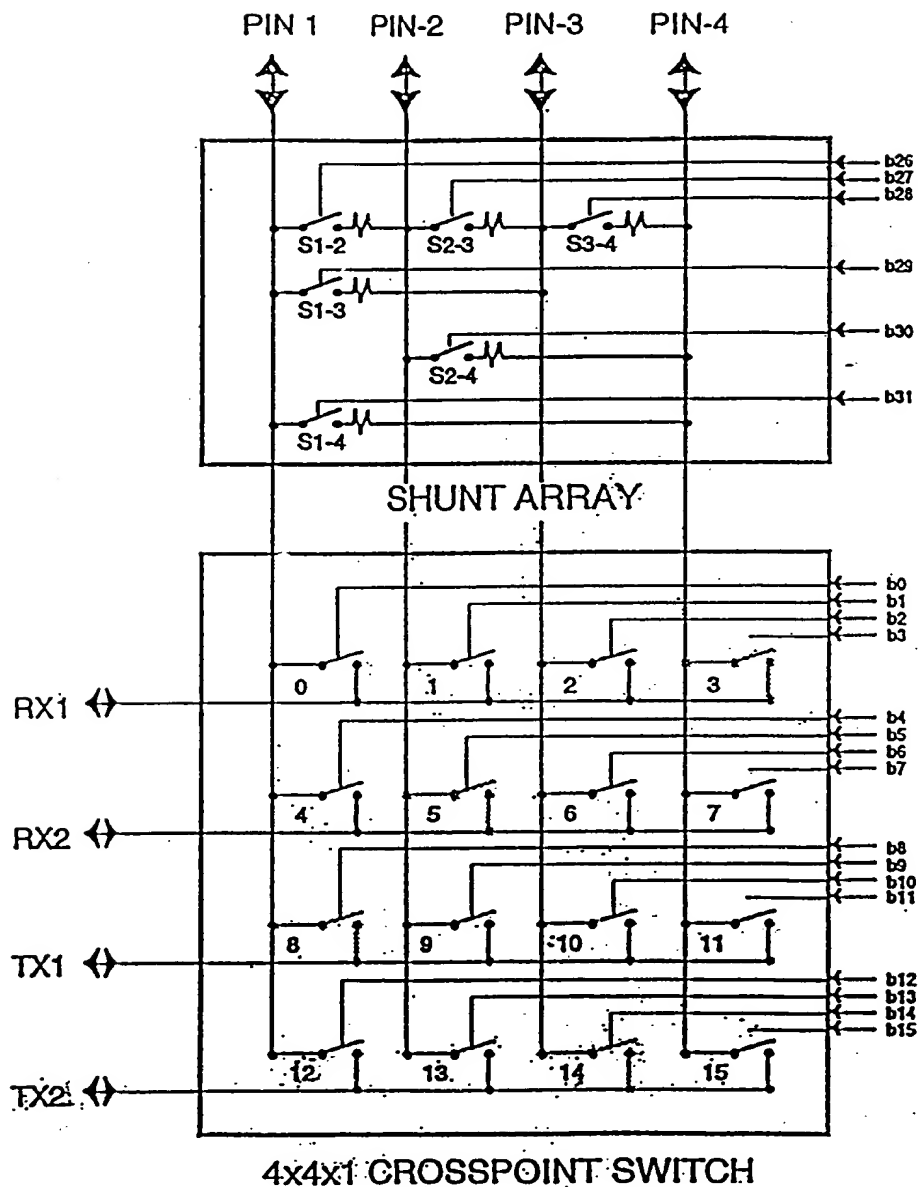


FIG. 9